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WPI Acc No: 76-H3457X/197633

Digital data recording circuit - has additional buffer
storage reducing asynchronous data losses

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Number of Countries: 001 Number of Patents: 001

Patent Family:

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| SU 491980 | A | 19760204 | | | | | 197633 B |

Priority Applications (No Type Date): SU 2007991 A 19740326

Abstract (Basic): SU 491980 A

Circuit contains code converter (1), controls (2), magnetic recording unit (3), input register (4), buffer storage units (5, 6) with address counters (7, 8) and buffer storage operation control commutator (9). Incoming data is applied to buffer storage through input register (4) with address counters sorting out recording sequence. Code converter forms multidigit data and control codes. Data loss is prevented using additional buffer storage and a commutator which at convenient moments and as a need arises (e.g. overflow) changes over from one storage to another to secure uniform data flow for recording.

Title Terms: DIGITAL; DATA; RECORD; CIRCUIT; ADD; BUFFER; STORAGE; REDUCE; ASYNCHRONOUS; DATA; LOSS

Derwent Class: T03

International Patent Class (Additional): G11B-005/02

File Segment: EPI

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